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EDUCATION

Ph.D.	Computer Science	University of California Riverside	2005
M.E.	Computer Science and Engineering	Huazhong University of Science and Technology, China	2000
B.E.	Computer Science and Engineering	Huazhong University of Science and Technology, China	1996

EMPLOYMENT

2005 – Present	Assistant Professor University of Massachusetts Lowell
Conduct research on network processors, low power microprocessor, Internet router and switch architecture, parallel and distributed processing, performance evaluation and design verification. Teach computer engineering courses at undergraduate and graduate levels.	
2001 – 2005	Graduate Student Researcher University of California, Riverside
Conducted research on network processors, low power microprocessor, Internet router and switch architecture, parallel and distributed processing, and design verification.	
2000 – 2005	Teaching Assistant University of California, Riverside
Taught lab sections of undergraduate computer science courses. Planned and prepared laboratory topics and materials. Lectured in laboratories and classrooms.	
1997 – 2000	Research Assistant Huazhong University of Science & Technology
Conducted research on cluster computing and real-time systems.	
1997 – 1998	Teaching Assistant Huazhong University of Science & Technology
Taught a number of computer science labs at undergraduate level. Planned and prepared laboratory topics and materials.	
1996 – 1997	IT Engineer, Research & Development Guangdong Nortel, China
System administration and technical support in full-time. Assisted testing of telecom switch hardware and software.	

RESEARCH INTEREST

Network processors, low power microprocessor, Internet router and switch architecture, parallel and distributed processing, software defined radio and performance evaluation.

PUBLICATIONS

Refereed Journal Papers

- Yan Luo, Jun Yang, Laxmi Bhuyan, Li Zhao, NePSim: A Network Processor Simulator with Power Evaluation Framework, *IEEE Micro Special Issue on Network Processors for Future High-End Systems and Applications*, Sept/Oct 2004.
- Yan Luo, Laxmi Bhuyan, Xi Chen, Shared Memory Multiprocessor Architectures for Software IP Routers, *IEEE Transactions on Parallel and Distributed Systems*, Vol 14, No. 12, Dec 2003
- Xi Chen, Yan Luo, Harry Hsieh, Laxmi Bhuyan and Felice Balarin, Assertion-Based Verification and Analysis of Network Processor Architectures, *International Journal of Design Automation for Embedded Systems*, Kluwer Academic Publishers.
- Jia Yu, Jun Yang, Shaojie Chen, Yan Luo, Laxmi Bhuyan, Enhancing Network Processor Simulation Speed with Statistical Input Sampling, *Lecture Notes in Computer Science*, Volume 3793, Oct 2005, Pages 68 - 83, Springer-Verlag Publishers

Refereed Conference Papers

- Li Zhao, Yan Luo, Laxmi Bhuyan, Ravi Iyer, SpliceNP: A TCP Splicer using a Network Processor, *ACM Symposium on Architecture for Networking and Communications Systems*, Princeton, NJ, Oct 27-28, 2005
- Jingnan Yao, Yan Luo, Laxmi Bhuyan, Ravi Iyer, Optimal Network Processor Topologies for Efficient Packet Processing, *IEEE Globecom 2005*, St Louis, MO, Nov 28-Dec 2, 2005
- Li Zhao, Yan Luo, Laxmi Bhuyan, Ravi Iyer, Implementation and Design of A Content-aware Switch Using A Network Processor, *IEEE Symposium on High Performance Interconnects*, Palo Alto, CA, August 17-19, 2005
- Yan Luo, Jia Yu, Jun Yang, Laxmi Bhuyan, Low Power Network Processor Design Using Clock Gating, *IEEE/ACM Design Automation Conference (DAC)*, Ahaheim, California, June 13-17, 2005
- Xi Chen, Yan Luo, Harry Hsieh, Laxmi Bhuyan, Felice Balarin, Utilizing Formal Assertions for System Design of Network Processors, Design Forum, *Design Automation and Test in Europe (DATE)*, 2004.
- Yan Luo, Li Zhao, Laxmi Bhuyan, Walid Najjar, Evaluating the Impact of Architectural Features on Communication Benchmarks, *Communications in Computing*, Nevada, USA, June 24-27, 2002
- David Watson, Yan Luo, and Brett Fleisch, Experiences with Oasis+: A Fault Tolerant Storage System, *Proceedings of the IEEE International Conference on Cluster Computing*, Newport Beach, CA, Oct 8-11 2001
- David Watson, Yan Luo, and Brett Fleisch, The Oasis+ Dependable Distributed Storage System, *Proceedings of the 2000 Pacific Rim International Symposium on Dependable Computing*, Los Angeles, CA, December 18-19, 2000

HONORS

Chancellor's Distinguished Fellowship	University of California Riverside, 2000-2005
Outstanding Student Award	Huazhong Univ. of Sci. & Tech., China, 1992-1996
Employee Award	Guangdong Nortel, China, 1997

AWARDS

Intel Corp., Deep Packet Inspection at Network Interfaces, PI, 01/06-01/07
Intel Corp., Equipment Grant, Oct 2005
ACM ANCS Travel Grant, Nov 2005

PROFESSIONAL ACTIVITIES

- Co-chair of UML-ADI National DSP Contest 2006
- Program Committee of the IEEE International Workshop on Multimedia Technology and Ubiquitous Computing (MTUC 2006), Taichung, Taiwan, June 5-7, 2006
- Paper reviewer for IEEE Transaction on Computers, Journal of System Architecture, IPDPS'04, ICPP'04, BEACON'04, DAC'05, IEEE Micro
- Academic Affairs Officer, Graduate Student Association of UCR, 2003-2004
- Graduate Student Committee Member, Computer Science Department of UCR, 2003 - 2004

PROFESSIONAL AFFILIATIONS

IEEE member, ACM member